

## HZT N-Channel SGT Power MOSFET

### Features

- N-channel
- $V_{DS} = 100V$ ,  $I_D = 200A$   
 $R_{DS(ON)} < 4m\Omega @ V_{GS} = 10V$  (Typ:  $3.0m\Omega$ )
- 100% avalanche tested
- Pb-free lead plating; RoHS compliant

### Application

- High performance SMPS, e.g. sync. rec.
- Hard Switching and High Speed Circuit
- Motor Control

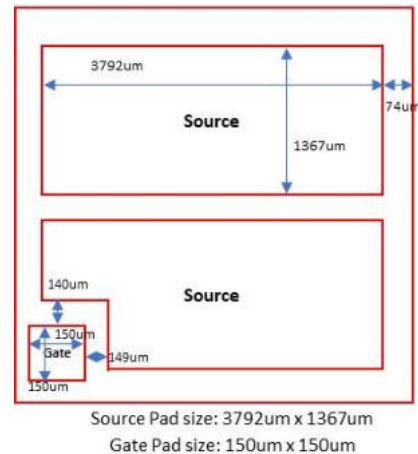
Wafer Size (inch)	8
Chip Size without scribe (mm)	3.94x3.01
Wafer Thickness (mil)	8
Top Metal	AlCu
Top Metal Thickness ( $\mu m$ )	4
Back Metal	Ti/Ni/Ag
Scribe Line ( $\mu m$ )	60
Gate Wire recommended	1*1.5mil Cu Wire
Source Wires recommended	15x 2mil Al Ribbon
Gross Die	2308

## 100V N-Ch Power MOSFET

Parameter	Value	Unit	
$V_{DS}$	100	V	
$R_{DS(on),typ}$	$V_{GS} = 10V$	$3.0$	$m\Omega$
$I_D$	200	A	

Unit:  $\mu m$

Die Size Without 60 $\mu m$  scribe line



**Electrical Characteristics at T<sub>j</sub>=25°C (unless otherwise specified)**
**Static Characteristics**

Parameter	Symbol	Test Condition	Value			Unit
			Min.	Typ.	Max.	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	100	110		V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.0	2.7	3.4	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{GS}=0V, V_{DS}=100V, T_j=25^\circ C$	-	0.01	1	$\mu A$
		$V_{GS}=0V, V_{DS}=100V, T_j=100^\circ C$		-	100	
Gate to Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	6.5	$\pm 100$	nA
Drain to Source on Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=2A$	-	3.0	4.0	m $\Omega$
		$V_{GS}=4.5V, I_D=2A$	-	-	-	
Gate Resistance	$R_G$	$V_{GS}=0V, V_{DS}=0V, f=1MHz$	-	-	-	$\Omega$